

ABSTRACT

A printed circuit board providing crosstalk compensation. The printed circuit board includes first plated through holes for receiving a first connecting component and second plated through holes for receiving a second connecting component. A signal carrying trace transmits a signal from one of the first plated through holes to one of the second plated through holes. A phase delay control trace is in electrical connection with the one of the first plated through holes. The phase delay control trace affects phase delay of the signal from the one of the first plated through holes to the one of the second plated through holes.